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Application Number:	10667491			
International Application Number:				
Confirmation Number:	1242			
Title of Invention:	Reduced circuit trace roughness for improved signal performance			
First Named Inventor/Applicant Name:	Robert Sheffield			
Correspondence Address:	Thomas E. Anderson Hunton & Williams LLP 1900 K Street, N.W. - Washington DC 20006-1109 US - -			
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Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Reply Brief Filed	57983rplybrf131.pdf	249090	no	7
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.